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Substitute for form 1449/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>				Complete if Known	
				Application Number	09/488,942
				Filing Date	1/21/2000
				First Named Inventor	Sherer
				Art Unit	2448
				Examiner Name	Strange, Aaron N.
Sheet	1	of	7	Attorney Docket Number	P190 1010.5

U.S. PATENT DOCUMENTS

Examiner Initials	Item	Document Number	Date	Name	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	1	3,786,432	01/15/1974	Woods	
	2	3,905,025	09/09/1975	Davis et al.	
	3	4,258,418	03/24/1981	Heath	
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	5	4,471,427	09/11/1984	Harris	
	6	4,546,467	10/08/1995	Yamamoto	
	7	4,590,467	05/20/1986	Lare	
	8	4,604,682	08/05/1986	Schwan et al.	
	9	4,672,570	06/09/1987	Benken	
	10	4,680,581	07/14/1987	Kozpik et al.	
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	13	4,768,190	08/30/1988	Giancarlo	
	14	4,807,117	02/21/1989	Itoku et al.	
	15	4,847,752	07/11/1989	Akashi	
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	19	4,987,535	01/22/1991	Takayama	
	20	5,043,981	08/27/1991	Firoozmand et al.	
	21	5,101,402	03/31/1992	Chin et al.	
	22	5,101,477	03/31/1992	Casper et al.	
	23	5,103,446	04/07/1992	Fischer	
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	25	5,121,390	06/09/1992	Farrell et al.	

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	26	5,121,479	06/09/1992	O'Brien	
	27	5,133,062	07/21/1992	Joshi et al.	
	28	5,133,078	07/21/1992	Minassian et al.	
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	31	5,146,595	09/08/1992	Fujiyama et al.	
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	41	5,289,579	02/22/1994	Punj	
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[illegible]

EXAMINER'S SIGNATURE:

DATE CONSIDERED:

PTO/SB/08b (03-09)

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				Examiner Name	Strange, Aaron N.
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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	59	Restriction Requirement dated 12/17/98 issued from parent application no. 09/028,088, filed 2/23/98	<input type="checkbox"/>
	60	Response to Restriction Requirement dated 12/17/98, filed 02/17/99, in parent application no. 09/028,088, filed 2/23/98	<input type="checkbox"/>
	61	Office Action dated 10/13/99 issued from parent application no. 09/028,088, filed 2/23/98	<input type="checkbox"/>
	62	Response to Office Action dated 10/13/99, filed 01/11/00, in parent application no. 09/028,088, filed 2/23/98	<input type="checkbox"/>
	63	Office Action dated 03/25/99 issued from parent application no. 09/028,088, filed 2/23/98	<input type="checkbox"/>
	64	Response to Office Action dated 03/25/99, filed 07/26/99, in parent application no. 09/028,088, filed 2/23/98	
	65	Notice of Allowance dated 02/03/00 issued from parent application no. 09/028,088, filed 2/23/98	
	66	Office Action dated 09/23/96 issued from parent application no. 08/503,797, filed 07/18/95	
	67	Response to Office Action dated 09/23/96, filed 01/23/97, in parent application no. 08/503,797, filed 07/18/95	
	68	Supplemental Remarks filed 04/23/97, in parent application no. 08/503,797, filed 07/18/95	
	69	Office Action dated 05/09/97 issued from parent application no. 08/503,797, filed 07/18/95	

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	70	Response to Office Action dated 05/09/97, filed 9/9/97, filed in parent application no. 08/503,797, filed 07/18/95			
	71	Office Action dated 11/21/97 issued in parent application no. 08/503,797, filed 07/18/95			
	72	Response to Office Action dated 11/21/97, filed 02/23/98, in parent application no. 08/503,797, filed 07/18/95			
	73	Notice of Allowance dated 03/11/98 issued in parent application no. 08/503,797, filed 07/18/95			
	74	Advanced Micro Devices, "The SUPERNET 2 Family for FDDI – 1991/1992 World Network Data Book" (1991), pages iii through 2-10, 2-40, 2-49 through 2-54			
	75	Tamir et al., "Design & Implementation of High-Speed Asynchronous Communication Ports for VLSI Multicomputer Nodes," ISCAS (1988)			
	76	Anzilotti et al., "Implementing a Non-Buffered Slave Ethernet Adapter using the Intel 82592," Intel Data Communications Focus Group (9/89)			
	77	Kwok et al., "Cut-Through Bridging for CSMA/CD Local Area Networks," IEEE Transactions on Communications, Vol. 38, No. 7 (July 1990)			
	78	Siegel, et al. "Overcoming Bottlenecks in High-Speed Transport Systems," IEEE (1991)			
	79	Chiang, AI, "An EtherStar is Born", ESD: Magazine, Digital Design Publishing Company, 3/88; 1900 West Park Drive, Westborough, MA 01581			
	80	EtherStar Ethernet/Starlan Controller, Fujitsu MB86950, May, 1988			
	81	Notice of Allowance dated 06/27/95 issued in parent application no. 08/374,491, filed 01/17/95			
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				Attorney Docket	P190 1010.5
83	Response to Office Action dated 10/26/93, filed 02/23/94, in co-owned application no. 07/920,893, filed 07/28/92				
84	Office Action dated 07/06/94 issued in co-owned application no. 07/920,893, filed 07/28/92				
85	Response to Office Action dated 07/06/94, filed 10/05/94, in co-owned application no. 07/920,893, filed 07/28/92				
86	Office Action dated 01/14/94 issued in parent application no. 07/907,946, filed 07/02/92				
87	Response to Office Action dated 01/14/94, filed 5/12/94, in parent application no. 07/907,946, filed 07/02/92				
88	Office Action dated 07/07/94 issued in parent application no. 07/907,946, filed 07/02/92				
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90	Notice of Allowance dated 10/18/94 issued in parent application no. 07/907,946, filed 07/02/92				
91	Fickel, Louise, "AMD Chip Set Promises to Bring FDDI Closer to the Desktop", InfoWorld, January 21, 1991, © 1991 IDG Communications, Inc., p. 8, et seq				
92	"AMD Introduces FDDI Chip for half-size card", The Local Area Network Magazine, February 1991, Vol. 6, No. 2, © 1991 Miller Freeman, Inc., p. 18, et seq				
93	Wilson, Ron, "FDDI chips struggle towards the desktop; Fibert Distributed Data Interface", Computer Design, February 1, 1991, Vol. 30, No. 3 © 1991 Pen Well Publishing Company. p. 61, et seq				
94	Preliminary Specification for the Am79C830A FORMAC Plus Chip Integrated Circuit, Advanced Micro Devices, July 1992, Publication No. 16287, pp. 1-140				
95	Information Disclosure Statement filed 12/17/96, in co-owned application no. 08/715,253, filed 09/16/96				

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Sheet	7	of	7	Attorney Docket	P190 1010.5
	96	Interview Summary dated 12/03/96 issued in co-owned application no. 08/715,253, filed 09/16/96			
	97	Office Action dated 01/07/97 issued in co-owned application no. 08/715,253, filed 09/16/96			
	98	Response to Office Action dated 01/07/97, filed 04/07/97, in co-owned application no. 08/715,253, filed 09/16/96			
	99	AMD, "THE SUPERNET 2 FAMILY FPOR FDDI", 1991/1992 World Network Book, (October, 1991), pp. 1-1 to A-4			
	100	Information Disclosure Statement filed 04/07/97 in co-owned application no. 08/715,253, filed 09/16/96			
	101	Terminal Disclaimer Under 37 C.F.R. §1.321(b), 35 U.S.C. §253, With Statement Under 37 C.F.R. §3.73(b) filed 04/07/97 in co-owned application no. 08/715,253, filed 09/16/96			
	102	Notice of Allowance dated 07/14/97 issued in co-owned application no. 08/715,253, filed 09/16/96			
	103	Preliminary Amendment filed 03/03/95 in co-owned application no. 08/397,997, filed 03/03/95			
	104	Information Disclosure Statement filed 08/14/95 in co-owned application no. 08/397,997, filed 03/03/95			
	105	Office Action dated 03/19/96 issued in co-owned application no. 08/397,997, filed 03/03/95			
Examiner Signature		/Aaron Strange/		Date Considered	04/06/2010

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Applicant's unique citation designation number (optional). * Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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